



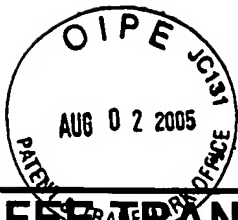
JKW ✓

TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Application No.	10/730,855
		Filing Date	December 8, 2003
		First Named Inventor	Soon-Yong Kweon
		Art Unit	2811
		Examiner Name	Vu, Hung K.
Total Number of Pages in This Submission	10	Attorney Docket Number	51876P422

ENCLOSURES (check all that apply)		
<input checked="" type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> PTO/SB/08 <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Basic Filing Fee <input type="checkbox"/> Declaration/POA <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <div style="border: 1px solid black; padding: 5px; margin-top: 10px;">return postcard; 2 cited references - copy of Notice of Preliminary Rejection</div>
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Eric S. Hyman, Reg. No. 30,139 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	7/18/05

CERTIFICATE OF MAILING/TRANSMISSION			
I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.			
Typed or printed name	Kumiko Alexander		
Signature		Date	7/29/05



FEE TRANSMITTAL for FY 2005

Patent fees are subject to annual revision.

Complete if Known

Application Number	10/730,855
Filing Date	December 8, 2003
First Named Inventor	Soon-Yong Kweon
Examiner Name	Vu, Hung K.
Art Unit	2811
Attorney Docket No.	51876P422

☐ Applicant claims small entity status. See 37 CFR 1.27.

TOTAL AMOUNT OF PAYMENT (\$) 180.00

METHOD OF PAYMENT (check all that apply)

☐ Check ☐ Credit card ☐ Money Order ☐ None ☐ Other (please identify): _____

☒ Deposit Account Deposit Account Number: 02-2666 Deposit Account Name: Blakely, Sokoloff, Taylor & Zafman LLP

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

☒ Charge fee(s) indicated below ☐ Charge fee(s) indicated below, except for the filing fee
☒ Charge any additional fee(s) or underpayment of fee(s) under 37 CFR §§ 1.16, 1.17, 1.18 and 1.20. ☐ Credit any overpayments

FEE CALCULATION

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
1051	130	2051	65	Surcharge - late filing fee or oath	
1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet.	
2053	130	2053	130	Non-English specification	
1251	120	2251	60	Extension for reply within first month	
1252	450	2252	225	Extension for reply within second month	
1253	1,020	2253	510	Extension for reply within third month	
1254	1,590	2254	795	Extension for reply within fourth month	
1255	2,160	2255	1,080	Extension for reply within fifth month	
1401	500	2401	250	Notice of Appeal	
1402	500	2402	250	Filing a brief in support of an appeal	
1403	1,000	2403	500	Request for oral hearing	
1451	1,510	2451	1,510	Petition to institute a public use proceeding	
1460	130	2460	130	Petitions to the Commissioner	
1807	50	1807	50	Processing fee under 37 CFR 1.17(q)	
1806	180	1806	180	Submission of Information Disclosure Stmt	180.00
1809	790	1809	395	Filing a submission after final rejection (37 CFR § 1.129(a))	
1810	790	2810	395	For each additional invention to be examined (37 CFR § 1.129(b))	

Other fee (specify) _____

SUBTOTAL (2) (\$) 180.00

SUBMITTED BY

Name (Print/Type)	Eric S. Hyman	Registration No. (Attorney/Agent)	30,139	Telephone	(310) 207-3800
Signature				Date	7/28/05



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

SOON-YONG KWEON

Application No.: 10/730,855

Filed: December 8, 2003

For: **FERROELECTRIC RANDOM ACCESS
MEMORY CAPACITOR AND METHOD
FOR MANUFACTURING THE SAME**

Art Group: 2811

Examiner: Vu, Hung K.

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted on or before payment of the Issue Fee. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

08/03/2005 MBERHE 00000025 10730855

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180.00 OP

The references were cited in a Search Report dated April 29, 2005 (copy enclosed herewith) in a counterpart Korean application, which was forwarded to Applicant's Representative in a communication dated June 29, 2005.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

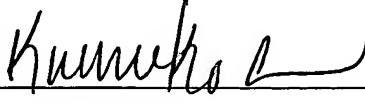
Date: _____

7/28/05


Eric S. Hyman, Reg. No. 30,139

12400 Wilshire Boulevard, 7th Floor
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 7/29/05

Kumiko Alexander Date



Information Disclosure Statement

U.S. Patent Application Serial No. 10/730,855

Your Ref. No.: 051876.P422

Our Ref. No.: P03H8030/US

Issued Date by the Korean Intellectual Property Office: April 29, 2005

Received Date from the Korean Intellectual Property Office: May 2, 2005

Reference No.: KR Laid-Open No. 2003-1217

JP Laid-Open No. 13-284548



(Translation)

NOTICE OF PRELIMINARY REJECTION

APPLICANT(S)

Name *Hynix Semiconductor Inc.*
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Kyoungki-do 467-860, Republic of Korea*

ATTORNEY(S)

Name *Shinsung International Patent & Law firm*
Address *2-3F, Line Bldg., Yeoksam-dong, Kangnam-gu
Seoul 135-080, Republic of Korea*

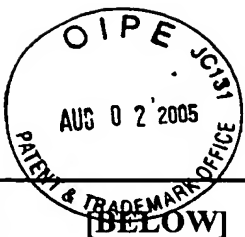
APPLICATION No. 2003-43176

TITLE *FERROELECTRIC CAPACITOR HAVING MERGED
TOP ELECTRODE PLATE LINE (MTP) STRUCTURE
AND METHOD FOR FABRICATING THE SAME*

This application was preliminarily rejected pursuant to Article 63 of the Korean Patent Law based on the following reason. Should there be any opinion against this action, please file a written argument by June 29, 2005. (You can apply for 1-month extension per each case, and we do not notify you of the confirmation for such term extension.)

[REASON]

This invention described in claims 1 to 7 can be easily invented by those skilled in the art as pointed out below. Accordingly, the above-identified patent application cannot be registered pursuant to Article 29, Paragraph 2 of the Korean Patent Law.



[BELOW]

This invention described in claims 1 to 7 relates to a ferroelectric capacitor including: a capacitor stack formed to have a width wider than that of a storage node contact; and a plate line formed on a top electrode of the capacitor stack and on predetermined portions of a second inter-layer insulation layer to have a width larger than that of the capacitor stack.

The KR Laid-Open No. 2003-1217 disclosed on January 6, 2003 (hereinafter referred as a cited reference 1) reveals a ferroelectric memory device including: a capacitor stack formed to have a width larger than that of a storage node contact; and a plate line formed on a top electrode of the capacitor stack and on predetermined portions of an inter-layer insulation layer to have a width larger than that of the capacitor stack. The JP Laid-Open No. 2001-284548 disclosed on October 12, 2001 (hereinafter referred as a cited reference 2) reveals a semiconductor memory device including: a capacitor stack formed to have a width larger than that of a storage node contact; and an interconnection line formed on a top electrode and on predetermined portions of an inter-layer insulation layer.

Accordingly, it is regarded that the present invention can be easily derivable by those ordinary skilled people from the cited references 1 and 2.

[Attachment] KR Laid-Open No. 2003-1217

JP Laid-Open No. 13-284548

Dated this 29th day of April, 2005

Examination Bureau
Examiner, Keun-Mo Kim

KOREAN INTELLECTUAL PROPERTY OFFICE